In the drawings:

Please amend the drawings as indicated on the enclosed Request for Drawing Change. No new matter has been added in the drawings

In the specification:

Please amend paragraph 14 as follows:

[14]   The signal **Sm** is sent to the ~~non-inverting~~ inverting input of a PWM comparator **5** whereas the signal **Srs** across the resistance **Rs** is provided to the non-inverting input. If the signals **Srs** and **Sm** are the same, the comparator **5** sends a signal to a control block **6** that pilots the transistor **M**, which in this case switches it off. In this way the output signal **Sm** of the multiplier determines the peak current of the transistor **M** and this is then enveloped by a rectified sinusoidal current. A filter at the stage input eliminates the switching frequency component so that the current absorbed by the network has the form of the sinusoidal envelope.